

5/3/2010  
DBBC

dbbcifa  
b      swap

Now dbbcifb (was a)  
= 1, agc, 1

62000 (need 15 db  
attenuation  
to bring down)

dbbcifa (was b)

now 32000 (filter 1)  
+ 4

Previously

dbbcifa      52000

dbbcfb      42000 (filter 4)  
    50 on filter 1 or 2

Jim Lovell

dd bs = 512k    if =  
                  of = 1der/sdb

1der/sda1

DBBC ctrl-S

P4

2.9 GHz

0.99 GB RAM

Jim / fermion

astronomer

AAEON

ASB - 835P Rev A 1.0  
1907835P01

→ 12.4 Version A Lab Tools

ChipScope - SFD.tar

Xilinx Programming Tools 13.1



→ Impact!

2 Traces not working.

FTP Max-Planck - will have image available

Priority New Flash Card be sent Friday.  
Patricia

1) I set ROP working

2) Install drivers + Xilinx s/w



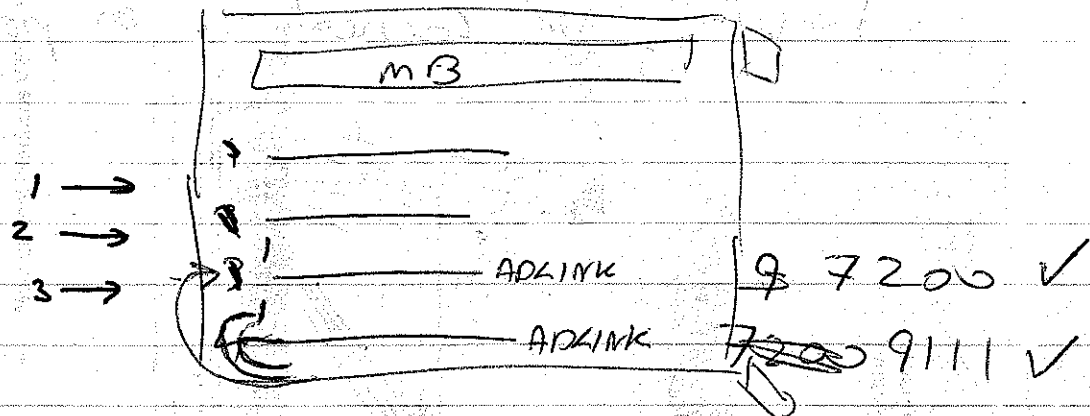
Skype with Gino.

Tele Conf next week.

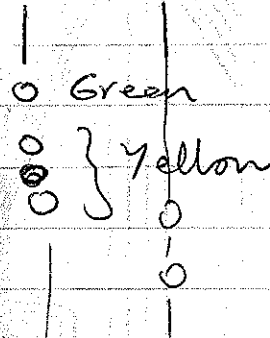


Fila IØG board - Yes available

PCI

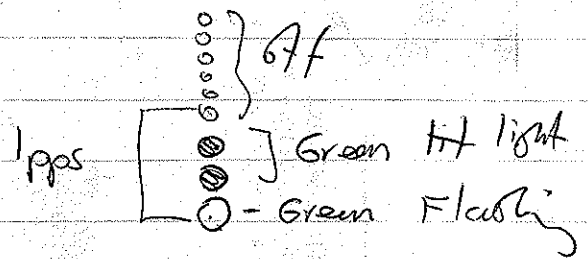


Front Board



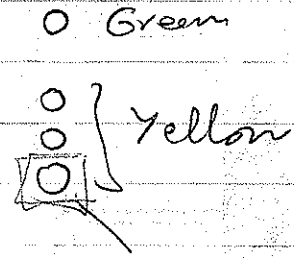
RST4

RST4+



Device Mgr

- L MurDAQ Boards
- L PCI 7200 Device
- L PCI 9111 Device



Device Mgr

(? Other Devices

- ?! L PCI Data Acquisition and Signal Processing Controller
- ?! L PCI Device

DRBC Control

L: Programming device ...

- 2
- 3
- 4

Dies!

update driver

